IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:		Confirmation No.
Applicant	:	Akai	
Filed	:	03-28-2006	
TC/A.U.	:		
Examiner	:		
Docket No.	:	TW08-P06092US	
Customer No.	:	33356	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Information Disclosure Statement

Dear Sir:

General Matters

X	Copies of the references listed on the attached form PTO/SB/08A are enclosed. Copies of US patents and patent applications are not enclosed.

This Information Disclosure Statement is being filed under:

X	37 CFR 1.97(b)(3)	Before the mailing of a first Office action on the merits.

X	**Concise Statement Under 37 CFR 1.98(3)(i):**
X	The following is a concise explanation of the relevance, as it is presently understood by the individual most knowledgeable about the content of the information, of each patent, publication, or other information listed that is not in the English language:
	1. JP 08-094984 -- This coating applicator includes a nozzle body which is disposed above an X-Y table 4, is driven along a Z direction and discharges the sealant to be applied on a substrate, a scope which picks up the image of the nozzle body and the substrate on the X-Y table from the side, an operating section which sets the spacing between the front end of the nozzle body and the front surface of the substrate by the image from the scope, a distance detecting sensor which detects the first height position on the front surface of the substrate when the spacing is set by the operating section and the second height position of the front surface of the substrate of the part to be coated with the sealant at the time of applying the sealant and a controller in which the first height position detected by the sensor is stored and which compares the second height position to be detected at the time of applying the sealant with the first height position and corrects the position of the Z direction of the nozzle body in accordance with the comparison.
	2. JP 2003-222886 -- In the liquid crystal device wherein a liquid crystal upper substrate having a liquid crystal upper electrode and a liquid crystal lower substrate having a liquid crystal lower electrode are disposed so that the liquid crystal upper electrode and the liquid crystal lower electrode are opposed to each other, the gap part therebetween is closely sealed by a sealing member having the liquid crystal injection port for forming a liquid crystal cell part and a liquid crystal is encapsulated in the liquid crystal cell part, limitation marks of end positions of the injection port are provided near the injection port. The limitation marks of the end position of the injection port are formed by using a transparent conductive film. The liquid crystal device has a color filter and the limitation mark of the end positions of the injection port is formed by using the color filter.
	3. JP 07-110489 -- The sealing material is formed between two sheets of the transparent substrates so as to go along the peripheral edge of the transparent substrate and constitute a terminal part terminating toward one peripheral edge side of the transparent substrates. Both transparent substrates are stuck to each other via this sealing material. The liquid crystal injection port is delineated by the terminal part of the sealing material between both transparent substrates and the inside and outside of the element is made communicatable via the liquid crystal injection port. The projecting part is formed to a band shape along the one peripheral edge side of the transparent substrates on the surfaces of the respective transparent substrates facing the liquid crystal injection port. The liquid crystal injection port is sealed to embed the projecting part by dropping and curing the sealing material so as to seal the liquid crystal material packed in the element.

	4. JP 2002-174819 -- Marks having a width corresponding to tolerance at the time of cutting are formed on the original substrate for an element substrate which constitutes a liquid crystal device so as to overlap a predetermined line. Therefore, if one part of the marks respectively remains on both sides of cut place when the original substrate is cut along the predetermined line, it is judged that the cutting is performed with the precision in tolerance. Therein, the liquid crystal filling port is opened between the marks on the cut place and, therefore, no trouble due to the quantity of the sealing material occurs by applying the sealing material so as not to reach the marks after filling the liquid crystal.
	5. JP 09-318957 -- The alignment marks for aligning the array substrate and counter substrate corresponding to the regions of the injection holes are arranged on the respective substrates. The respective alignment marks to be arranged on the array substrate are formed by film forming and patterning simultaneously with the formation of scanning lines and gate electrodes and are formed to drop-out cruciform marks. The respective alignment marks arranged at the counter substrate are formed by film forming and patterning simultaneously with the formation of light shielding films and are formed to approximate cruciform shapes. The alignment marks for aligning a pair of the electrode substrate to each other are arranged in the manner described above, by which the utilization efficiency of the substrates is enhanced and the excellent productivity is assured.
	6. JP 2002-303842 -- The liquid crystal device comprising a two-screen driving liquid crystal display panel is provided with a plurality of 1st electrodes which are arranged on a 1st substrate forming the liquid crystal panel and separated into two in the longitudal direction, a plurality of 2nd electrodes which are arranged on a 2nd substrate stuck together with the 1st substrate with a sealing material and faced so as to be orthogonal to the 1st electrodes, and alignment marks which are arranged on the 1st and 2nd substrates, respectively, and formed in the same shape as that of the arrangement the 1st or 2nd electrodes in the separated area of the 1st electrodes, and is characterized in that the center of the alignment mark is almost positioned on the center line of the separated area of the 1st electrodes.
	7. JP 2000-039599 -- This device is provided with a vertical illumination optical system consisting of a vertical illumination mirror barrel illuminating a liquid crystal display substrate loaded on an XY stage from just above to obtain an optical image on the liquid crystal display substrate and a CCD camera and an optical cut-out optical system consisting of slit projection optical X,Y mirror barrels tilted at 45 degrees on the liquid crystal substrate to illuminate and obtain the optical image on the liquid crystal display substrate, observation optical system X,Y mirror barrels and CCD cameras to obtain the optical images of the cut-off part of the liquid crystal display substrate and an oriented film, a seal agent and a conductive pattern for every inspection coordinates on the liquid crystal display substrate by these CCD cameras. They are recognized as images by a personal computer to obtain the sizes of these widths and heights, etc., and to make difference values between the obtained sizes and the reference sizes display on a summing up monitor.

Deposit Account Authorization

Please apply any charges or credits to Deposit Account No. 503456.

Respectfully submitted,

Date: March 28, 2006

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	
Filing Date	03-28-2006
First Named Inventor	Akai
Art Unit	
Examiner Name	
Attorney Docket Number	TW08-P06092US

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 5,936,695	08-10-1999	Hida	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3] Number[4] Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T[6]
		JP 08-094984	04-12-1996	Hara Akira		
		JP 2003-222886	08-08-2003	Tanaka Katsuyuki		
		JP 07-110489	04-25-1995	Ono Koichi		
		JP 2002-174819	06-21-2002	Kobayashi Masaki		
		JP 09-318957	12-12-1997	Hida Yoshihito		
		JP 2002-303842	10-18-2002	Nakamura Takeshi		

Examiner Signature		Date Considered	

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Sheet 2 of 2

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		JP 2000-039599	02-08-2000	Maruyama Shigenobu		

Examiner Signature		Date Considered	

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